

**APPLICATION DATA SHEET****Application Information**

Application number::

Filing Date::

Application Type:: Regular

Subject Matter:: Utility

Suggested classification::

Suggested Group Art Unit::

CD-ROM or CD-R?:: None

Number of CD disks::

Number of copies of CDs::

Sequence submission?::

Computer Readable Form (CRF)?:: No

Number of copies of CRF::

Title :: LINEAR SCALABLE FFT/IFFT COMPUTATION  
IN A MULTI-PROCESSOR SYSTEM

Attorney Docket Number:: 852463.406

Request for Early Publication?:: No

Request for Non-Publication?:: No

Suggested Drawing Figure::

Total Drawing Sheets:: 4

Small Entity?:: No

Petition included?:: No

Petition Type::

Licensed U.S. Gov't Agency::

Contract or Grant No::

Secrecy Order in Parent Appl.?:: No

**First Applicant Information**

Applicant Authority Type:: Inventor  
Primary Citizenship Country:: India  
Status:: Full Capacity  
Given Name:: Kaushik  
Middle Name::  
Family Name:: Saha  
Name Suffix::  
City of Residence:: Delhi  
State or Province of Residence::  
Country of Residence:: India  
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State or Province of mailing address::  
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**Second Applicant Information**

Applicant Authority Type:: Inventor  
Primary Citizenship Country:: India  
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Given Name:: Srijib  
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State or Province of mailing address::  
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**Correspondence Information**

Correspondence Customer Number :: **38106**

**Representative Information**

Representative Customer Number::		<b>38106</b>
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**Domestic Priority Information**

Application ::	Continuity Type::	Parent Application::	Parent Filing Date::

**Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
India	1208Del/2002	12/03/02	Yes

**Assignee Information**

Assignee name::	STMicroelectronics Pvt. Ltd.
Street of mailing address::	Plot No. 2 & 3, Sector 16A, Institutional Area, Noida
City of mailing address::	Uttar Pradesh
State or Province of mailing address::	
Country of mailing address::	India
Postal or Zip Code of mailing address::	201 301

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